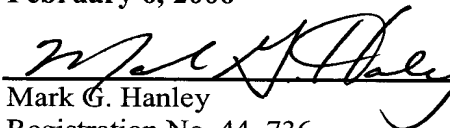




PATENT
Docket No. 20004/3US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Daniel Nelson)	I hereby certify that this paper is
)	being deposited with the United
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Filed: November 9, 2001)	Box 1450, Alexandria, VA 22313-
)	1450 on this date
)	
For: APPARATUS AND METHOD FOR)	February 6, 2006
DETECTING AND CORRECTING A)	
CORRUPTED BROADCAST TIME)	
CODE)	Mark G. Hanley
)	Registration No. 44, 736
Group Art Unit: 2133)	Attorney for Applicant(s)
)	
)	
Examiner: F. Alphonse)	

LETTER TO THE DRAFTSMAN

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

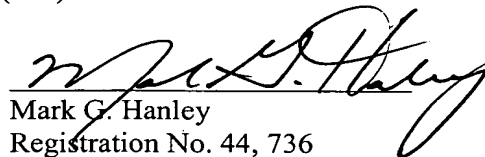
Sir:

Applicant is submitting attached to this paper a replacement drawing for FIG. 2 in above mentioned application. In the replacement drawing, the lead line for the temporary memory buffer 37 is extended from the box next to the memory device 35 instead of the processor 32. Applicant respectfully submits that this modification corrects an error in the drawing and does not add any new matter. Applicant respectfully requests entry of this replacement drawing.

Respectfully submitted,

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By:


Mark G. Hanley
Registration No. 44, 736

February 6, 2006